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APPLICATION NO.	FI	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/629,559	10/629,559 07/30/2003		Tetsuya Nagata	501.42964X00	6532
20457	7590	06/22/2006		EXAM	INER
		RY, STOUT & KR	TON, MINH TOAN T		
1300 NORTH SEVENTEENTH STREET SUITE 1800				ART UNIT	PAPER NUMBER
ARLINGTO		22209-3873	2871		

DATE MAILED: 06/22/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)
		10/629,559	NAGATA ET AL.
	Office Action Summary	Examiner	Art Unit
		Toan Ton	2871
Period fo	The MAILING DATE of this communication app	pears on the cover sheet with the c	correspondence address
A SHO WHIC - Exter after - If NO - Failu Any r	ORTENED STATUTORY PERIOD FOR REPL' CHEVER IS LONGER, FROM THE MAILING D. Isions of time may be available under the provisions of 37 CFR 1.1 SIX (6) MONTHS from the mailing date of this communication. period for reply is specified above, the maximum statutory period or te to reply within the set or extended period for reply will, by statute eply received by the Office later than three months after the mailing and patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tir will apply and will expire SIX (6) MONTHS from a, cause the application to become ABANDONE	N. nely filed the mailing date of this communication. ED (35 U.S.C. § 133).
Status			
2a)⊠	Responsive to communication(s) filed on <u>12 A</u> This action is FINAL . 2b) This Since this application is in condition for allowar closed in accordance with the practice under E	s action is non-final. nce except for formal matters, pro	
Dispositi	on of Claims		
5)□ 6)⊠ 7)□ 8)□	Claim(s) <u>1,4-8,11-13,15-17,21-25,27 and 28</u> is 4a) Of the above claim(s) is/are withdraw Claim(s) is/are allowed. Claim(s) <u>1, 4-8, 11-13, 15-17, 21-25 and 27-28</u> Claim(s) is/are objected to. Claim(s) are subject to restriction and/or	wn from consideration. 8 is/are rejected.	
Applicati	on Papers		
10)	The specification is objected to by the Examine The drawing(s) filed on is/are: a) acc Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Ex	epted or b) objected to by the drawing(s) be held in abeyance. Se tion is required if the drawing(s) is ob	e 37 CFR 1.85(a). ojected to. See 37 CFR 1.121(d).
Priority u	ınder 35 U.S.C. § 119		
12)[_ a)[Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority document application from the International Bureasee the attached detailed Office action for a list	ts have been received. ts have been received in Applicat rity documents have been receive u (PCT Rule 17.2(a)).	ion No ed in this National Stage
2)	t(s) e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail D 5) Notice of Informal F 6) Other:	

Application/Control Number: 10/629,559 Page 2

Art Unit: 2871

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

2. Claims 1, 4-8, 11-13, 15-17, 21-25 and 27-28 are rejected under 35 U.S.C. 103(a) as being unpatentable over Morosawa et al (JP 06-132306, IDS reference) in view of Baek (US 6657689).

Morosawa discloses an active matrix substrate comprising (see at least Figure 2): thin film transistors characterized in that each thin film transistor includes a silicon film, a gate electrode, and a source electrode; between the silicon film and the substrate and between the electrode and the substrate, a silicon oxide film and a silicon nitride film are formed, wherein the silicon nitride (SiN) film is formed between the silicon oxide film and the substrate.

Morosawa discloses (see at least Figure 2) a silicon oxide film (a thickness of 1000Angtroms) and a silicon nitride film formed on the surface of the substrate (a thickness of 1000-4000A, larger than 1000 Angstroms, overlapping Applicant's range of 130-160 nm, 126-165 nm and 118-169 nm) for advantages such as achieving excellent quality for the silicon film. It is noted that it has been held that overlapping ranges are at least obvious. Therefore, it would have been at least obvious to one of ordinary skill in the art at the time the invention was made to employ a silicon nitride film and a silicon oxide formed on the surface of the substrate for advantages such as achieving excellent quality for the silicon film.

Application/Control Number: 10/629,559

Art Unit: 2871

In general, a liquid crystal display (LCD) is classified as a transmission type and a reflection type depending on implementing an internal or external light source. The transmission type uses a backlight; and the reflection type comprises uses ambient light. However, the transmission type LCD comprises problems such as high power consumption, and the reflection type LCD comprises problems such as low visibility in dark environment (see Baek, at least in background of the invention)

These problems are solved through the use of a transflective type LCD device, wherein this type of LCD device realizes both a transmissive mode display and a reflective mode display (see at least Figure 2: the pixel electrode includes a reflective electrode and a light-transmissive electrode, a distance from the substrate to the reflective electrode is different from the a distance from the substrate to the light-transmissive electrode; an insulator is formed between the reflective electrode and a substrate; a backlight is formed outside the substrate). Therefore, it would have been at least obvious to one of ordinary skill in the art at the invention was made to employ a transflective display mode for achieving advantages such as bright ambient light and low power consumption (see Baek, at least in background of the invention).

Morosawa in view of Baek yields a similar structural device as Applicant's claimed invention, and thus limitations such as 'the silicon nitride and silicon oxide films are configured so as to reduce reflection from a transmissive region of the liquid crystal display device', 'the film thickness of the silicon nitride film has a thickness value which minimizes reflection light from the transmissive region of the liquid crystal device' are at least functional limitations/expected results (if not inherent).

Semiconductor elements such as TFT are commonly used and known to be included in LCD devices for advantages such as cross-talk reduction. Morosawa discloses a typical TFT element that would be commonly used in LCD devices. Therefore, it would have been at least obvious to one of ordinary skill in the art at the time the invention was made to employ semiconductor elements such as TFT, as commonly used and known in the art, for advantages such as cross-talk reduction.

The use of an IPS type LCD device is known in the art for providing advantages such as large viewing angle due to at least the formation of the pixel electrode and the common electrode formed on the same substrate with an insulation (organic) film there between.

Therefore, it would have been at least obvious to one of ordinary skill in the art at the time the invention was made to employ an IPS type LCD device for achieving advantages such as large viewing angle.

Response to Arguments

3. Applicant's arguments filed 04/13/06 have been fully considered but they are not persuasive.

Morosawa in view of Baek yields a similar structural device as Applicant's claimed invention, and thus limitations such as 'the silicon nitride and silicon oxide films are configured so as to reduce reflection from a transmissive region of the liquid crystal display device', 'the film thickness of the silicon nitride film has a thickness value which minimizes reflection light from the transmissive region of the liquid crystal device' are at least functional limitations/expected results (if not inherent).

Application/Control Number: 10/629,559

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The use of an IPS type LCD device is known in the art for providing advantages such as large viewing angle due to at least the formation of the pixel electrode and the common electrode formed on the same substrate with an insulation (organic) film there between.

Therefore, it would have been at least obvious to one of ordinary skill in the art at the time the invention was made to employ an IPS type LCD device for achieving advantages such as large viewing angle. It is noted that Applicant has not challenged that the use of an IPS type LCD device is not well known in the art.

Conclusion

4. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

TOANTON PEIMARY EXAMINER

Contact Information

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Toan Ton whose telephone number is (571) 272-2303.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

June 20, 2006